

Appl. No. 10/064,601
Amdt. dated February 10, 2006
Reply to Office action of November 10, 2005

Amendments to the Specification:

The Examiner objects to the following informalities found in paragraphs 44 and 45 of the disclosure: "look-up table" should be "look-up table 50". The applicant has fixed the informalities.

- 5 Please replace paragraph [44] with the following amended paragraph:

Thus in either of the two approaches described above, the controller 42, using the calculation logic 46, determines the target frequency for the DPLL signal. The controller 42 then outputs the second control signal from the DAC 48 to the VCO 38 to generate the DPLL signal at the target frequency. In practical application, the
10 second control signal can be retrieved from a look-up table 50, which can be stored in the controller 42, by referencing the target frequency.

Please replace paragraph [45] with the following amended paragraph:

During the seek mode the controller 42 sets charge pump currents of the frequency detector 32 and the phase detector 34 according to the target frequency to adjust the
15 response time of the control circuit 30. When the charge pump currents are increased, response of the control circuit 30 is relatively fast. When the charge pump currents are decreased, response of the control circuit 30 is slower. The controller 42 also sets an RF equalizer signal and a differential phase detector (DPD) equalizer signal, both of which are used to filter signals coming from the optical pickup head, according to
20 the target frequency. In practical application, the RF equalizer signal and the DPD equalizer signal can also be retrieved from a look-up table 50, which can be stored in the controller 42, by referencing the target frequency.